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APPLICANTS

James W. Cady, Austin, TX;
 James Wilder, Austin, TX;
 David L. Roper, Austin, TX; James Douglas Wehrly JR., Austin, TX;
jbe

**** CONTINUING DATA *******

This application is a CIP of PCT/US03/29000 09/15/2003
 which is a CON of 10/453,398 06/03/2003
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jbe

**** FOREIGN APPLICATIONS *******
jbe

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
**** 06/15/2004**

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY TX	SHEETS DRAWING 34	TOTAL CLAIMS 19	INDEPENDENT CLAIMS 4
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged <i>James W. Cady</i> Examiner's Signature <i>jbe</i> Initials				

ADDRESS
 J. Scott Denko
 Andrews Kurth, LLP
 Suite 1700
 111 Congress Ave.
 Austin, TX
 78701

TITLE
 Integrated circuit stacking system and method

<input type="checkbox"/> All Fees
<input type="checkbox"/> 1.16 Fees (Filing)